



Tech Info Library

PowerPC: SPEC ratings for 604 & 620 chips (10/96)

Article Created: 19 July 1995

Article Reviewed/Updated: 23 October 1996

TOPIC -----

This article contains the SPEC ratings for the PowerPC 604 and 620 chips used in Power Macintosh computers. MIPS (Millions of Instructions Per Second) ratings are not provided for PowerPC chips.

Note: This information comes from Motorola and IBM. For additional information, contact Motorola or IBM's web sites at:

Motorola: <http://www.mot.com/SPS/PowerPC/>

IBM: <http://www.chips.ibm.com/products/ppc/index.html>

For additional information about SPEC ratings, contact <http://www.specbench.org/>.

DISCUSSION -----

PowerPC Chip:	604	620
Technology:	0.5 micron CMOS process	0.5 micron CMOS process
Die Size:	12.4mm x 15.8mm (196mm ²)	17.1mm x 18.2mm (311mm ²)
Transistors:	3.6 million (1.5 million for logic)	7 million
Cache:	32K Split	64K Split
Temp. Range:	Not Available	Not Available
Performance:	160 SPECint92 @ 100MHz(proj) 165 SPECfp92 @ 100MHz(proj)	225 SPECint92 @ 133MHz(est) 300 SPECfp92 @ 133MHz(est)
Signal I/Os:	171	482
Power Supply:	3.3V	3.3V±0.3V
Power Dissipation:	<10W @ 100MHz (est)	30W @ 133MHz

Packaging: Ceramic quad flat pack 25 x 25 Ball Grid Array
(256 pins)

Article Change History:
23 Oct 1996 - Added URLs.

Copyright 1995-96, Apple Computer, Inc.

Keywords: kppc

=====

This information is from the Apple Technical Information Library.

19961023 16:31:12.00

Tech Info Library Article Number: 18205