



# Tech Info Library

## Macintosh LC: Expansion Slot

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### TOPIC -----

This article is about the Macintosh LC's PDS (processor-direct slot) expansion slot.

### DISCUSSION -----

You can expand the Macintosh LC via a 96-pin expansion connector located on the main logic board. This is Apple's first 68020 PDS, requiring a new slot definition. Expansion cards will be approximately 3-inches by 7-inches in size and will lie horizontally above the logic board within the Macintosh LC. An opening in the back of the Macintosh LC accommodates a connector the size of a DB-15. The expansion connector is supplied with the following voltages, and must be limited to the following current load:

Voltage	Current
+5	800 mA
-5	20 mA
+12	200 mA

Power limitations allow dissipation of no more than 4 watts, all of which can be taken from +5, or from any combination of the three voltages -- but the total cannot exceed 4 watts.

Here are the pinouts for the Macintosh LC PDS connector for columns A, B, and C:

- A1 - Sound
- A2 - /SLOTIRQ
- A3 - /AS
- A4 - /DSACK1
- A5 - /HALT 68020 Halt
- A6 - FC2
- A7 - FC0
- A8 - /RMC
- A9 - D31 Data bit 31

A10 - D28 Data bit 28  
A11 - D25 Data bit 25  
A12 - D22 Data bit 22  
A13 - D19 Data bit 19  
A14 - D16 Data bit 16  
A15 - D13 Data bit 13  
A16 - D10 Data bit 10  
A17 - /BGACK  
A18 - A1 address bit 1  
A19 - A26 address bit 26  
A20 - A23 address bit 23  
A21 - A20 address bit 20  
A22 - /  
A23 - D2 Data bit 2  
A24 - D1 Data bit 1  
A25 - A4 address bit 4  
A26 - A6 address bit 6  
A27 - A11 address bit 11  
A28 - A9 address bit 9  
A29 - A16 address bit 16  
A30 - A18 address bit 18  
A31 - FAN  
A32 - +12V

B1 - Analog Ground  
B2 - R/W  
B3 - +5V  
B4 - +5V  
B5 - SIZ1  
B6 - Ground  
B7 - C16M  
B8 - Ground  
B9 - D30 Data bit 30  
B10 - D27 Data bit 27  
B11 - D24 Data bit 24  
B12 - D21 Data bit 21  
B13 - D18 Data bit 18  
B14 - D15 Data bit 15  
B15 - D12 Data bit 12  
B16 - D9 Data bit 9  
B17 - /BR  
B18 - A31 address bit 31  
B19 - A25 address bit 25  
B20 - A22 address bit 22  
B21 - /IPL2  
B22 - D3 Data bit 3  
B23 - D5 Data bit 5  
B24 - D0 Data bit 0  
B25 - A2 address bit 2  
B26 - A12 address bit 12  
B27 - A13 address bit 13  
B28 - A8 address bit 8  
B29 - A15 address bit 15

B30 - A17 address bit 17  
B31 - A// Clock  
B32 - Ground

C1 - /FPU  
C2 - /DS  
C3 - /BERR  
C4 - /DSACK0  
C5 - SIZ0  
C6 - FC1  
C7 - /RESET  
C8 - /BG  
C9 - D29 Data bit 29  
C10 - D26 Data bit 26  
C11 - D23 Data bit 23  
C12 - D20 Data bit 20  
C13 - D17 Data bit 17  
C14 - D14 Data bit 14  
C15 - D11 Data bit 11  
C16 - D8 Data bit 8  
C17 - A0 address bit 0  
C18 - A27 address bit 27  
C19 - A24 address bit 24  
C20 - A21 address bit 21  
C21 - /IPL1  
C22 - D4 Data bit 4  
C23 - D6 Data bit 6  
C24 - D7 Data bit 7  
C25 - A3 address bit 3  
C26 - A5 address bit 5  
C27 - A7 address bit 7  
C28 - A10 address bit 10  
C29 - A14 address bit 14  
C30 - A19 address bit 19  
C31 - FC3  
C32 - -5V

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