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## 68040 and 68LC040 Microprocessor: Specifications (2/93)

Article Created: 9 February 1993

TOPIC -----

This article provides technical specifications of the 68040 and 68LC040 microprocessor.

DISCUSSION -----

### Performance

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#### 68040

- 22 MIPS and 3.5 MFLOPS at 25 MHz
- 29 MIPS and 4.6 MFLOPS at 33 MHz
- 35 MIPS and 5.6 MFLOPS at 40 MHz
- 1.3 clock cycles per instruction

#### 68LC040

- 17.6 MIPS at 20 MHz
- 22 MIPS at 25 MHz
- 29 MIPS at 33 MHz
- 1.3 clock cycles per instruction

### Manufacturing Technology

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- 0.65  $\mu\text{m}$  HCMOS
- 1.2 million transistors

### System Components

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- 4KB instruction cache
- 4KB data cache
- Instruction memory management unit
- Data memory management unit
- 6-stage pipeline integer unit
- 32-bit data bus
- Floating-point unit (except 68LC040)

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Keywords: SPECSHT

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19960215 11:05:19.00

Tech Info Library Article Number: 11371