



Tech Info Library

AppleLine: Interface Specifications (Discontinued)

1. Interface Form: RS 232C with hardware handshaking
2. Data Form: Asynchronous serial ASCII
3. Byte Length: 7 or 8 bits
 - switch selectable (see 12.a.)
4. Transmission Format:
 - a. Start signal: 1 bit
 - b. Stop signal: 0, 1, 1.5, or 2 bits
 - Switch selectable (see 12.c.)
5. Parity: None, Odd, or Even
 - switch selectable (see 12.b.)
6. Transmission speed: 75, 150, 300, 600, 1200, 2400, 4800, or 9600 baud
 - switch selectable (see 12.d.)
7. Input buffer capacity: 255 bytes
8. Interface connector: DB-25SA-J4
 - standard 25-pin RS232
9. Pin assignments:

Pin Assignment

 - a. 1: Protective ground
 - b. 3: Data input (from computer)
 - c. 4: +12 VDC
 - d. 7: Data return (signal ground)
 - e. 20: Data terminal ready (to computer)
10. Signal levels:
 - a. On (Space): +5 V to +15 V
 - b. Off (Mark): -15 V to -5 V

11. Signal load impedance: 3000 to 7000 ohms

12. DIP Switch assignments:

ON
OFF

1 2 3 4 5 6 7 8

a. Data length (data bits):

1. 7 bits On
2. 8 bits Off

1

b. Parity:

1. No Parity Off
2. Parity On
3. Odd On
4. Even Off

2 3

c. Stop Bit:

1. None On On
2. 1 Off On
3. 1.5 On Off
4. 2 Off Off

4 5

d. Baud Rate:

1. 75 Off Off Off
2. 150 Off Off On
3. 300 Off On Off
4. 600 Off On On
5. 1200 On Off Off
6. 2400 On Off On
7. 4800 On On Off
8. 9600 On On On

6 7 8

Copyright 1985, 1992, Apple Computer, Inc.

Keywords: SPECSHT

=====

This information is from the Apple Technical Information Library.

19960215 11:05:19.00

Tech Info Library Article Number: 1515