

Tech Info Library

Network Server 500 & 700: DRAM Configurations (3/96)

Article Created: 15 February 1996 Article Reviewed/Updated: 22 March 1996
TOPIC
This article describes DRAM configurations, both parity and nonparity, for the Network Server 500 and 700 computers.
DISCUSSION

DRAM is provided in packages called Dual Inline Memory Modules, or DIMMs.

Your server can work with any of several DRAM configurations, depending on the density of the DRAM chips that are mounted on the DIMMs. The 168-pin parity DIMMs used in the Network Server are available from Apple in sizes of 8, 16, and 32 MB pairs, enabling you to increase Network Server memory by 16, 32, and 64 MB, respectively. Your server can have a maximum of 512 MB of memory. The DIMM operating range is 5 volts.

The Network Server 500 and 700 do not have any DRAM installed on the logic board. There exists 8 DIMM slots, where memory should be added in pairs. The maximum amount of memory that each server can hold is 512MB of RAM, obtained by placing 64MB DIMMs in all 8 DIMM slots.

IMPORTANT: The DIMMs should be 72-bit-wide 168-pin fast-paged mode, 60-nanosecond (ns) RAM access time or faster. DRAM must support byte writes. SIMMs and some DIMMs from older Macintosh computers will not work in the Network Server. The parity DIMMS should be installed in matched pairs (for example, two 16 MB DIMMs, one in slot 1A, the other in slot 1B).

Nonparity DRAM with 70-nanosecond DRAM access time or faster will work; however, if there is any nonparity DRAM installed, all server parity checking is disabled. Consult an Apple-authorized dealer or service provider for information and upgrades.

Parity DIMMs should be added in pairs, however it is not necessary. If they are added in pairs, 144 bit wide data path is gained by interleaving the parity DIMMs.

The Network Server 700 comes in two configurations, a 32 or 48 MB configuration. These configurations can be obtained in three ways based on the memory simm chip inventory available at the time of build. Unfortunately we can not predict

or guarantee which configuration a customer will receive.

32MB: 1 32 MB parity DIMMs.

2 16 MB parity DIMMs.

4 8 MB parity DIMMs.

48MB: 1 32MB parity DIMM, and 1 16MB parity DIMM

2 16MB parity DIMMs, and 2 8MB parity DIMMS

3 16MB parity DIMMs

Article Change History:

22 Mar 1996 - Added information about configuration and DIMMs.

26 Feb 1996 - Changed distribution status.

Copyright 1996, Apple Computer, Inc.

Keywords: supt, hts

This information is from the Apple Technical Information Library.

19960325 08:28:28.00

Tech Info Library Article Number: 19361