

POWER SUPPLY & READ ONLY MEMORY SECTIONS

sheet 1 of 6

APPLE M5000 "MacINTOSH II" COMPUTER LOGIC BOARD

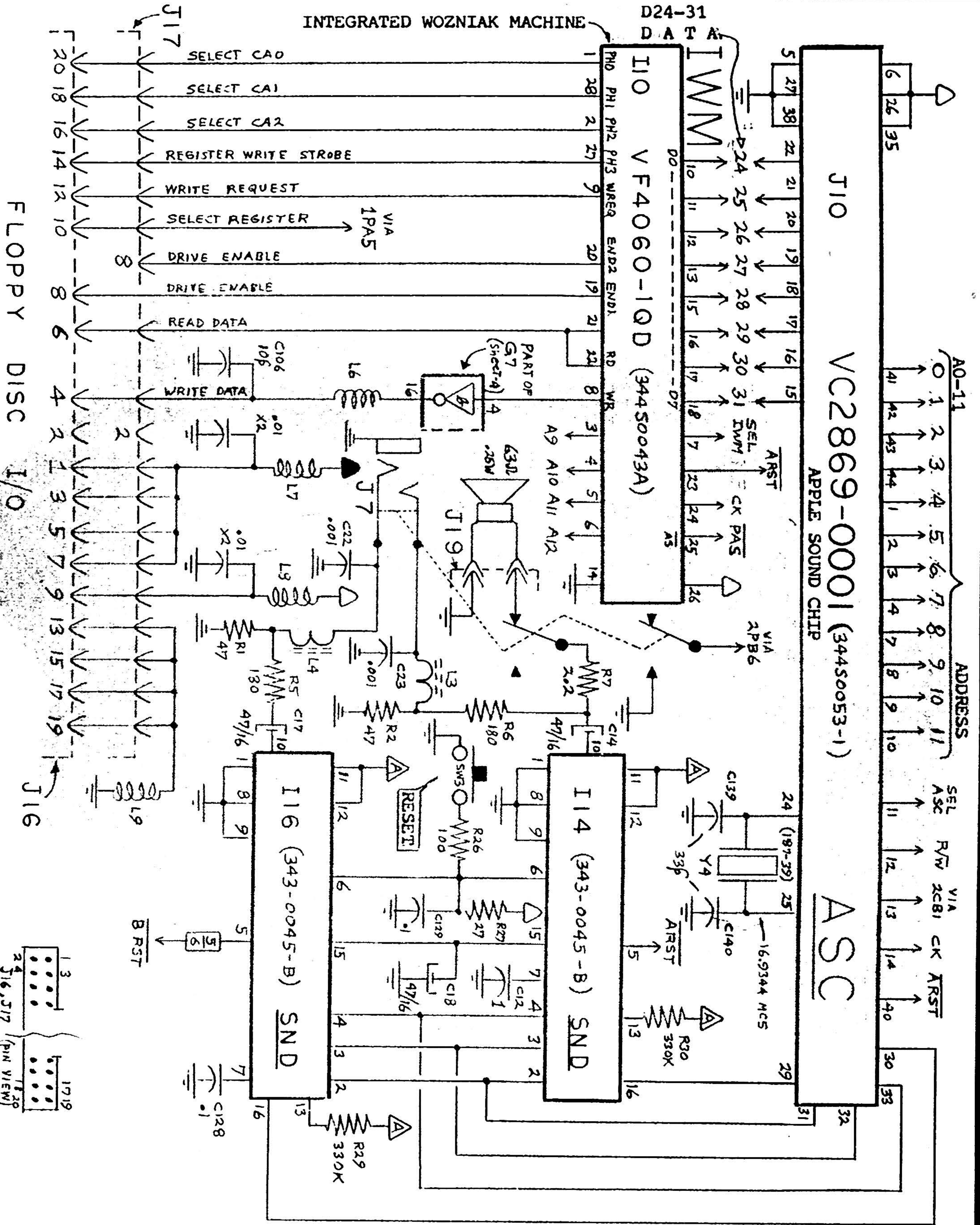
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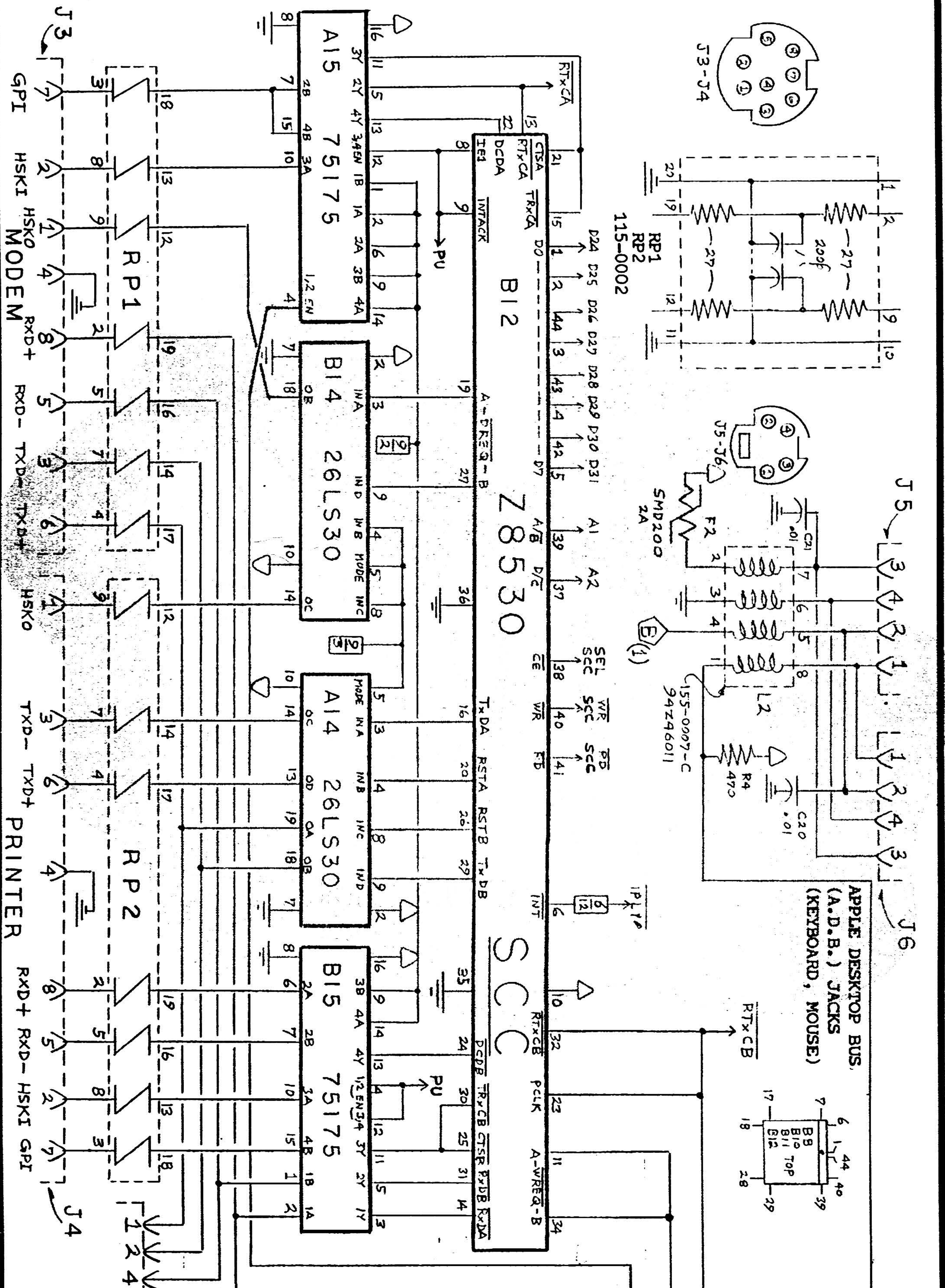
8/28/89-

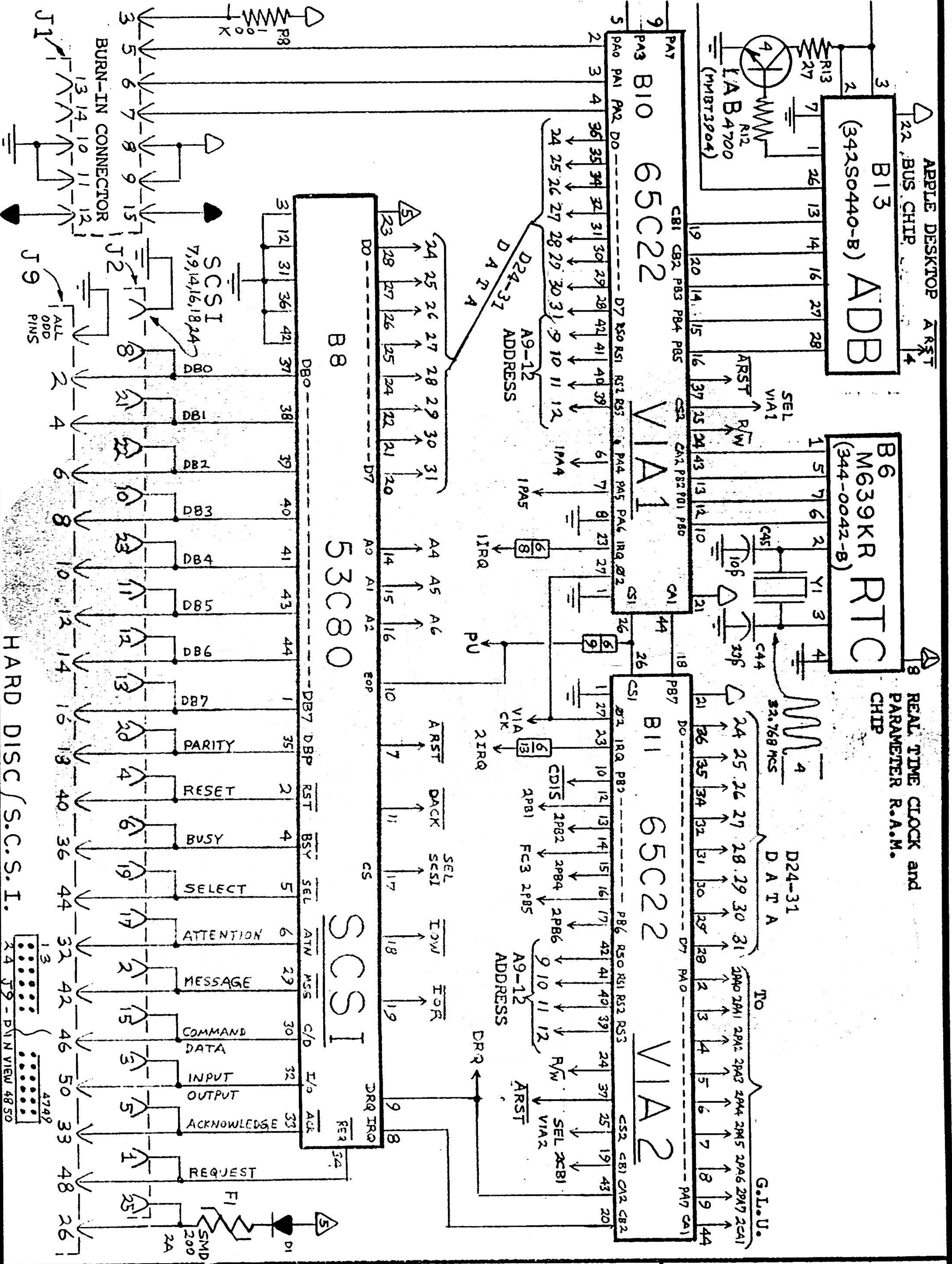
INTEGRATED WOZNIAK MACHINE

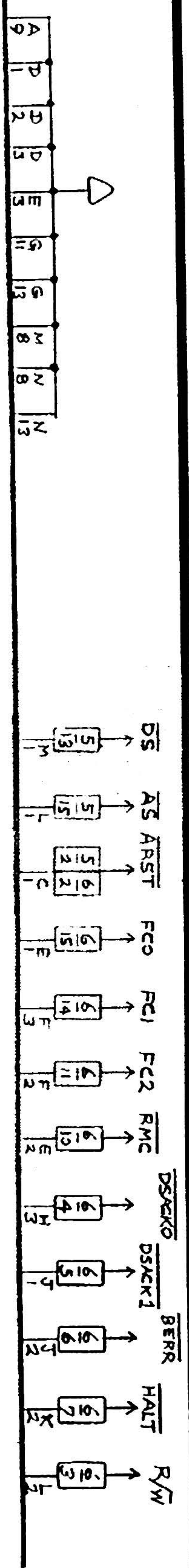
D24-31

D A T A



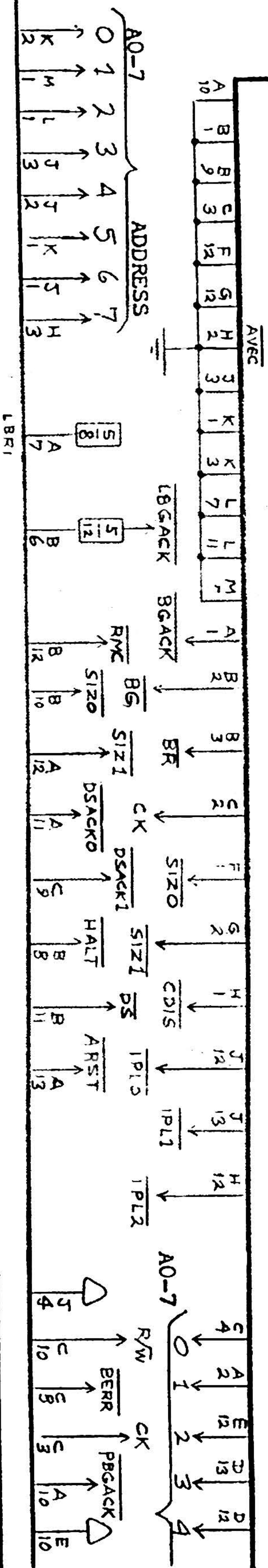






I 13

MC6

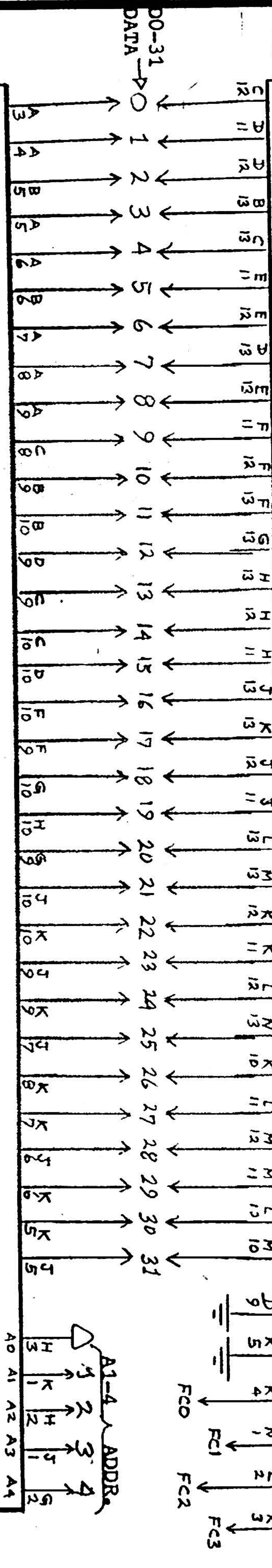
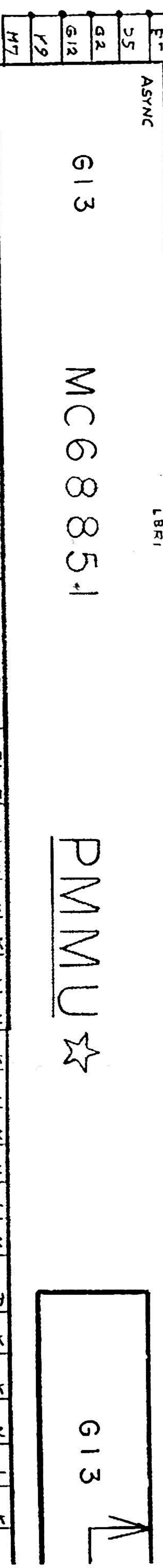


G 13

MC68851

PMMU

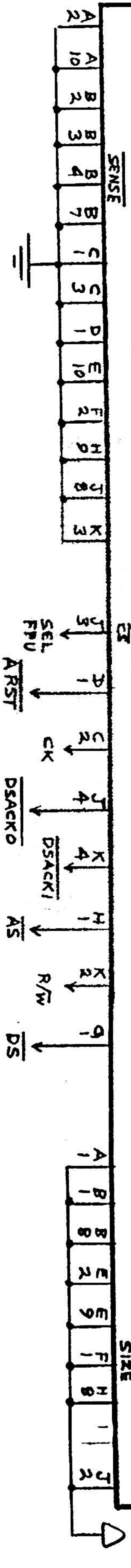
G 13

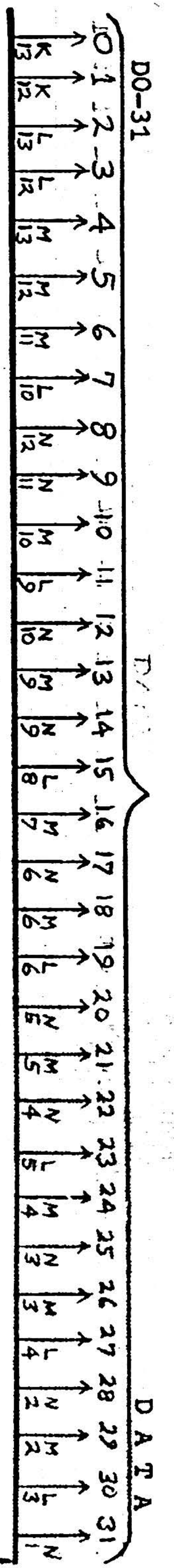


I 11

MC68881

FPU

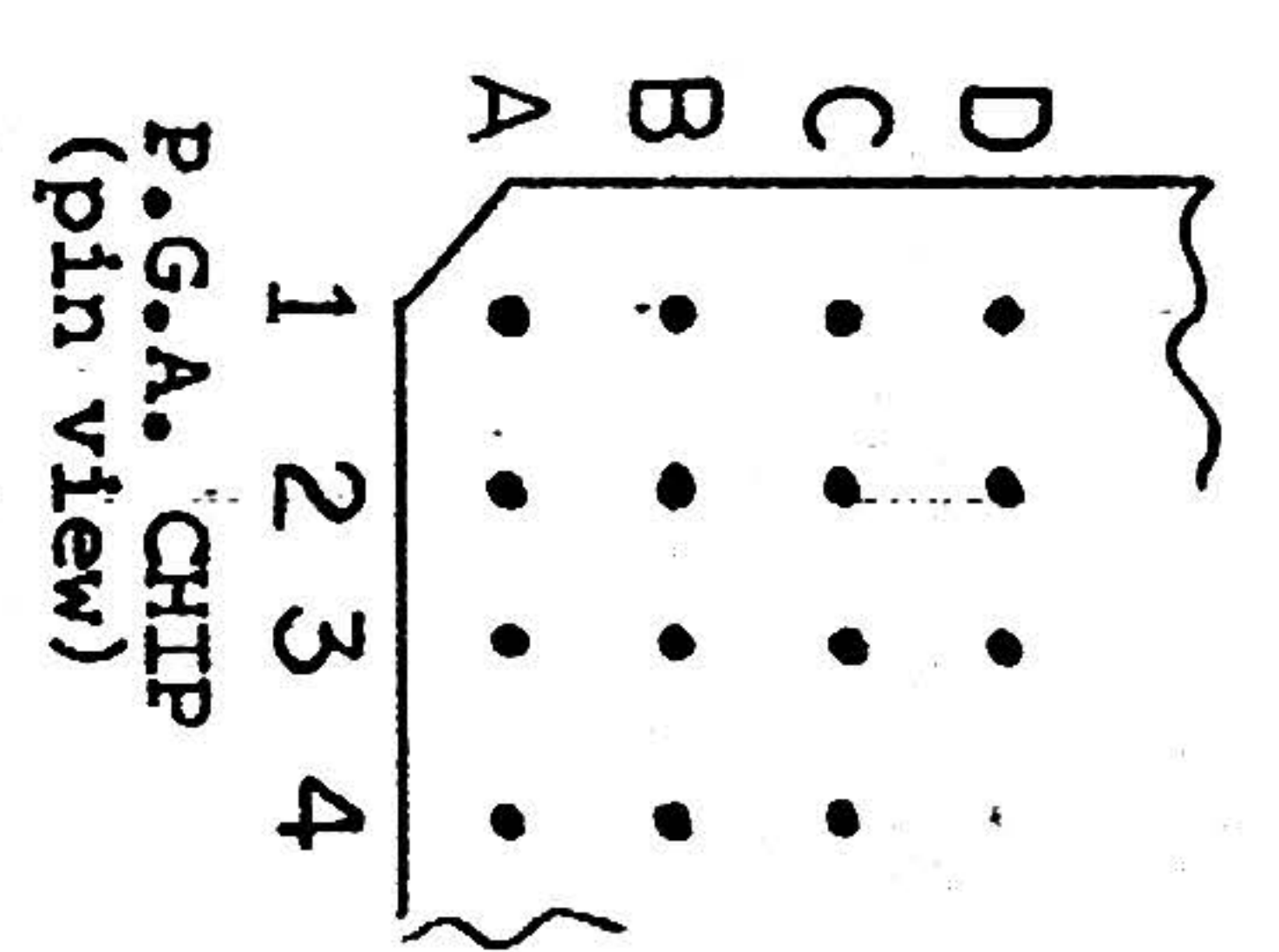
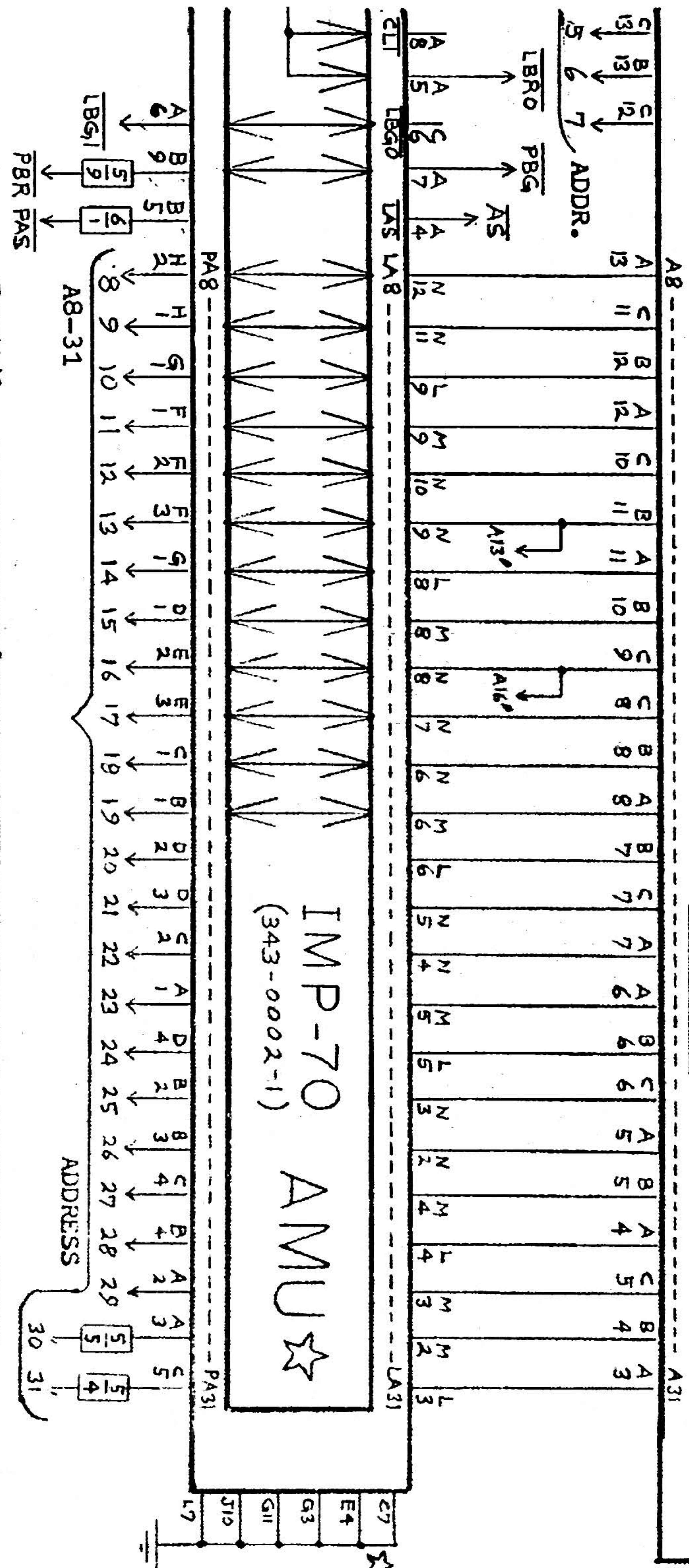




8020

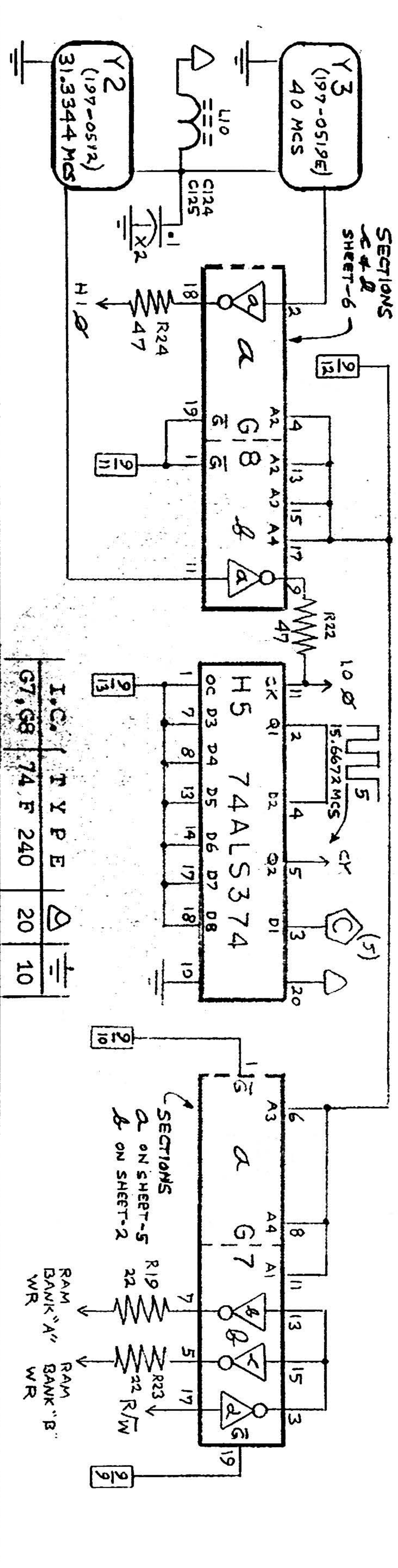
MPU

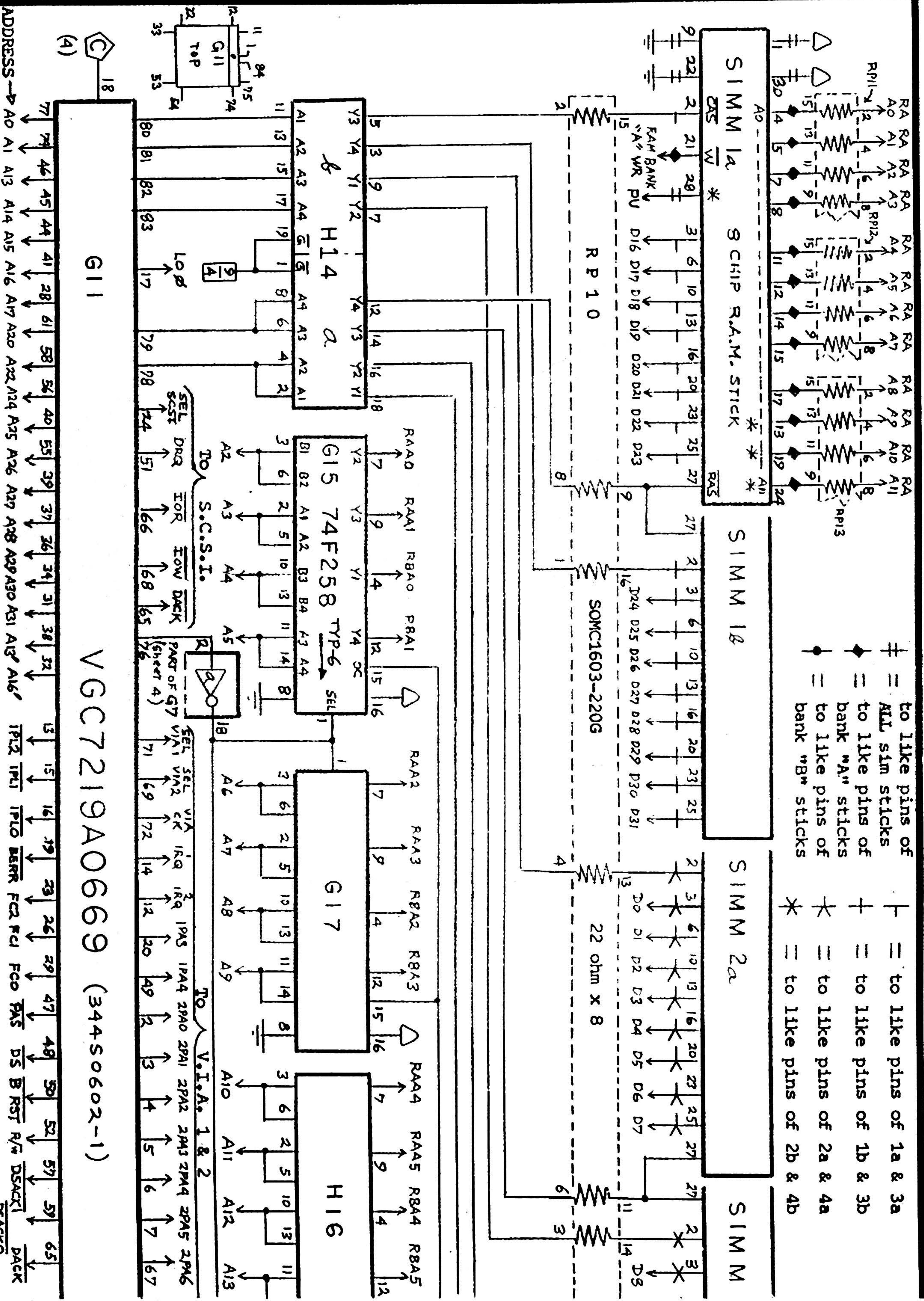
IMP-70 AMU (343-0002-1)



RP# resistor arrays (sheet - 1)

A.M.U. is an alternate hybrid chip and jumper array. If P.M.M.U. is used, outer ring applies. If A.M.U. is used, inner ring applies and jumpers are shown as: ←→





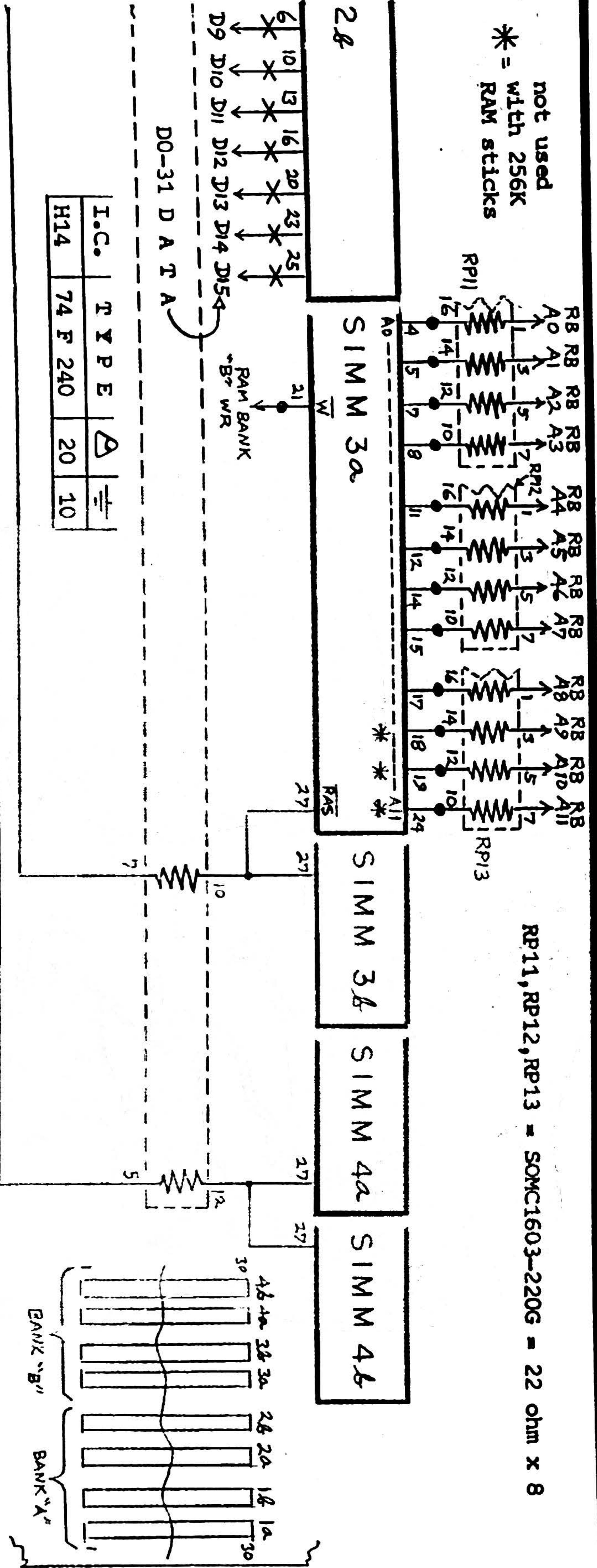
- ⊕ = to like pins of ALL sim sticks
- ◆ = to like pins of bank "A" sticks
- = to like pins of bank "B" sticks
- ⊥ = to like pins of 1a & 3a
- ⊥ = to like pins of 1b & 3b
- ⊥ = to like pins of 2a & 4a
- * = to like pins of 2b & 4b

VGC7219A0669 (344S0602-1)

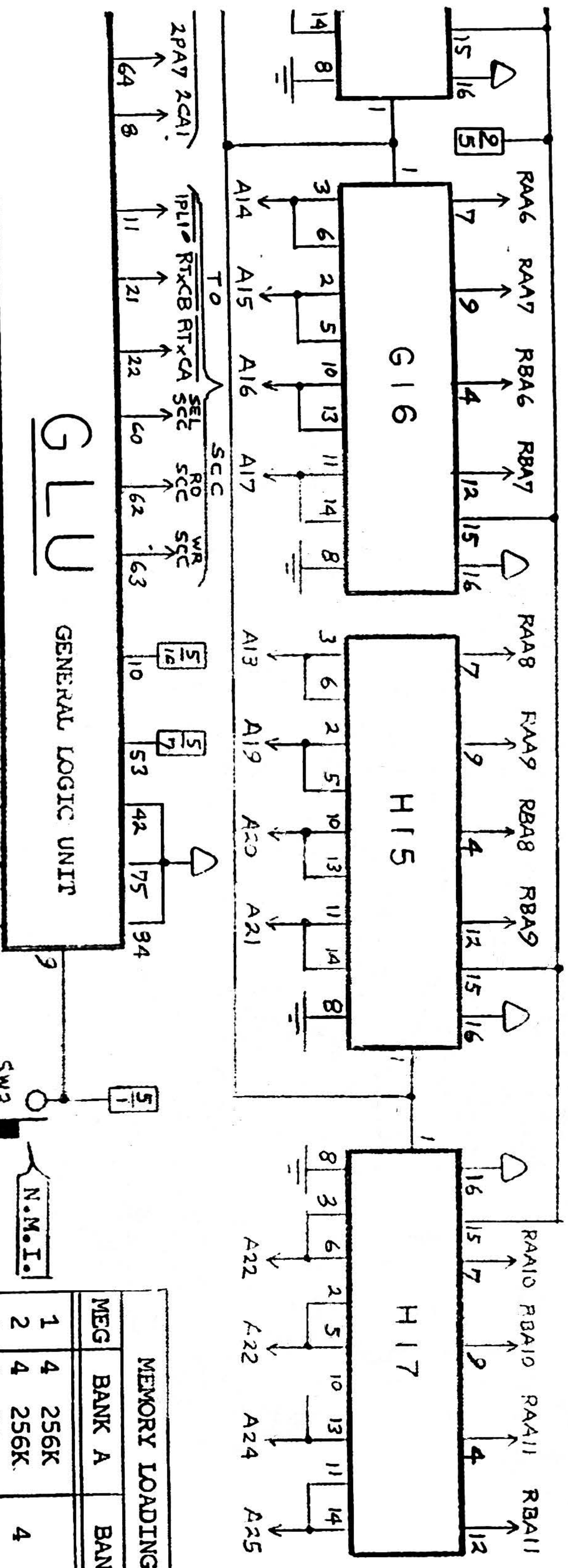
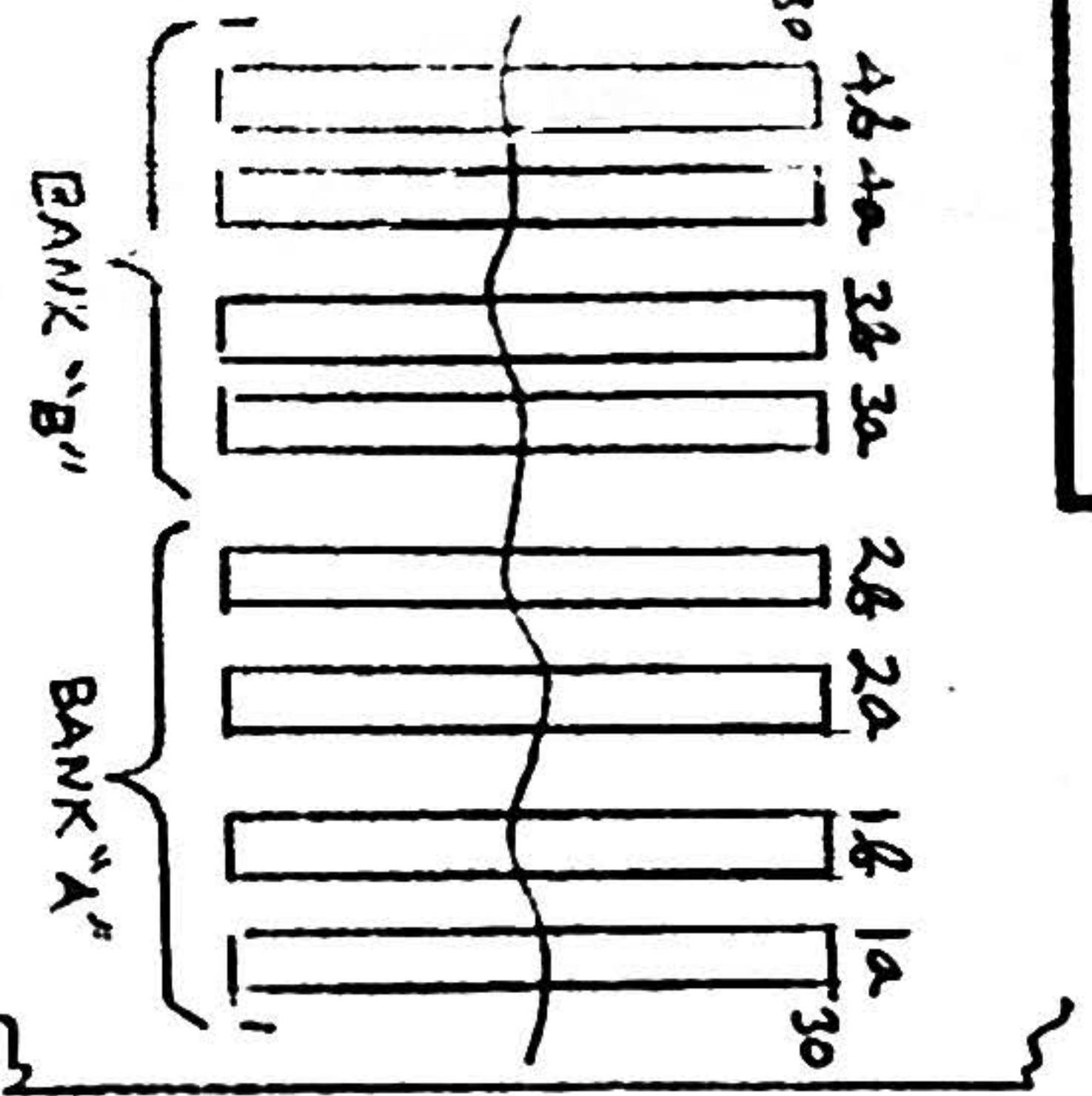
ADDRESS → A0 A1 A13 A14 A15 A16 A17 A20 A22 A24 A25 A26 A27 A28 A29 A30 A31 A13' A16' IP12 IP1 IPLO BERR FC2 FC1 FCO PAS DS B RST R/W DSACK1 DACK DSACK0

* = not used
 * = with 256K
 RAM sticks

RP11, RP12, RP13 = SOMC1603-220G = 22 ohm x 8



I.C.	TYPE	QTY
H14	74F240	20
		10



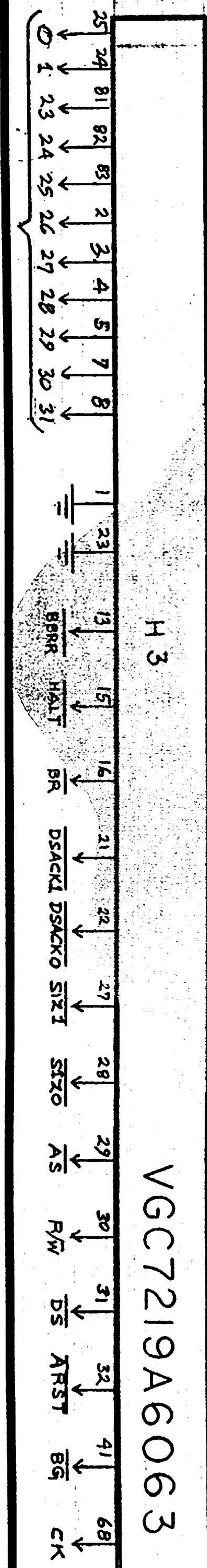
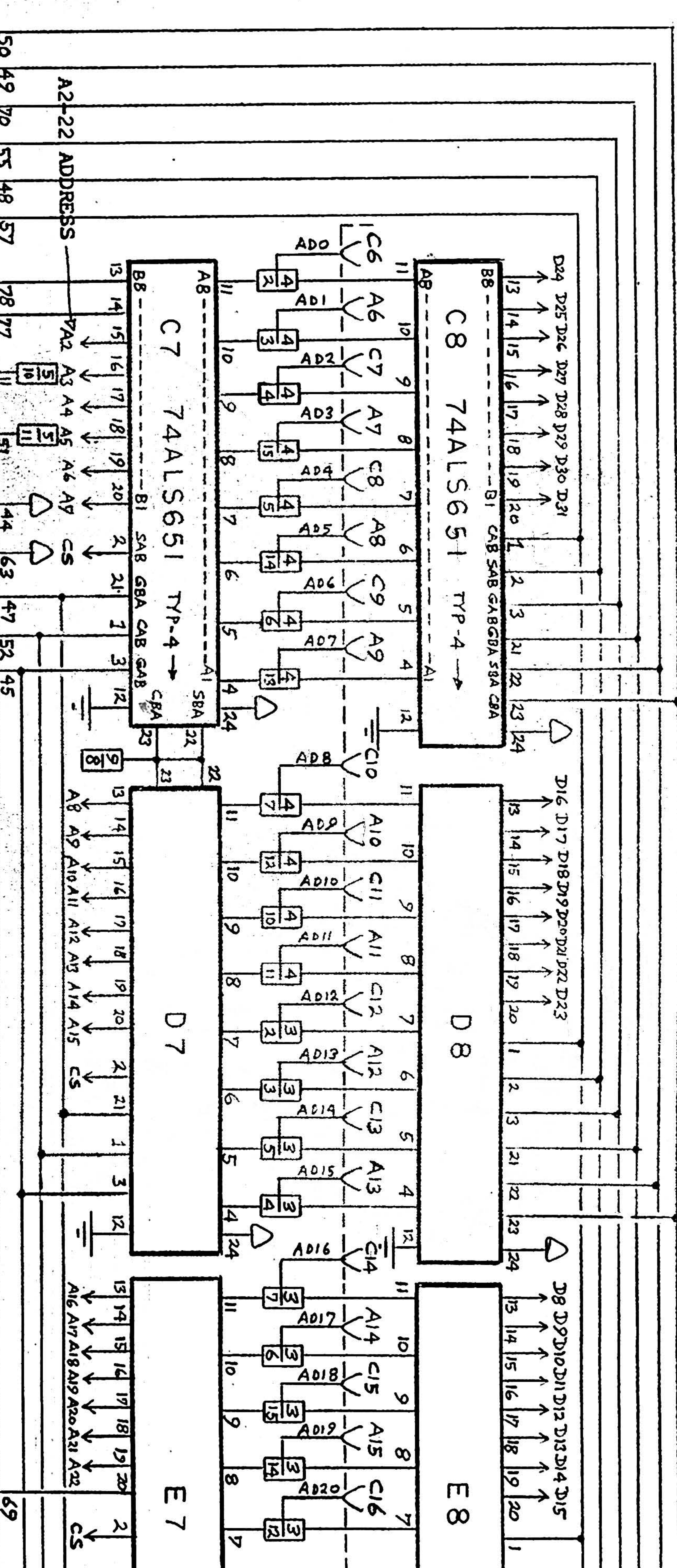
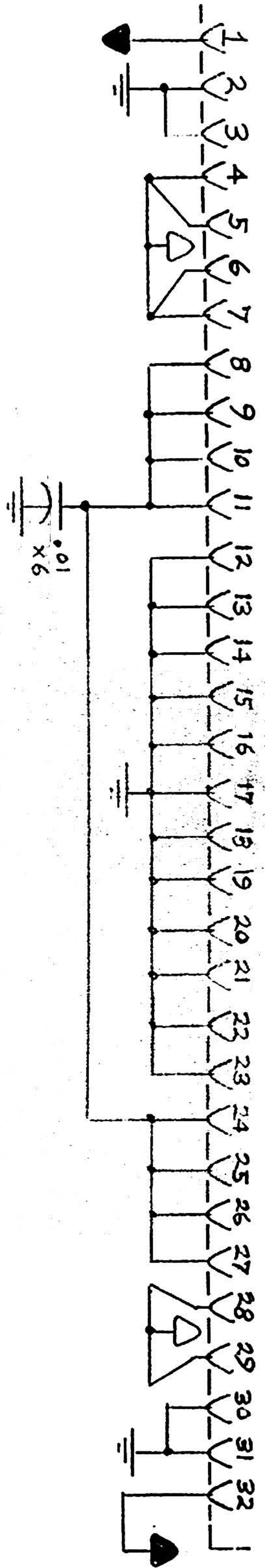
GLU

GENERAL LOGIC UNIT

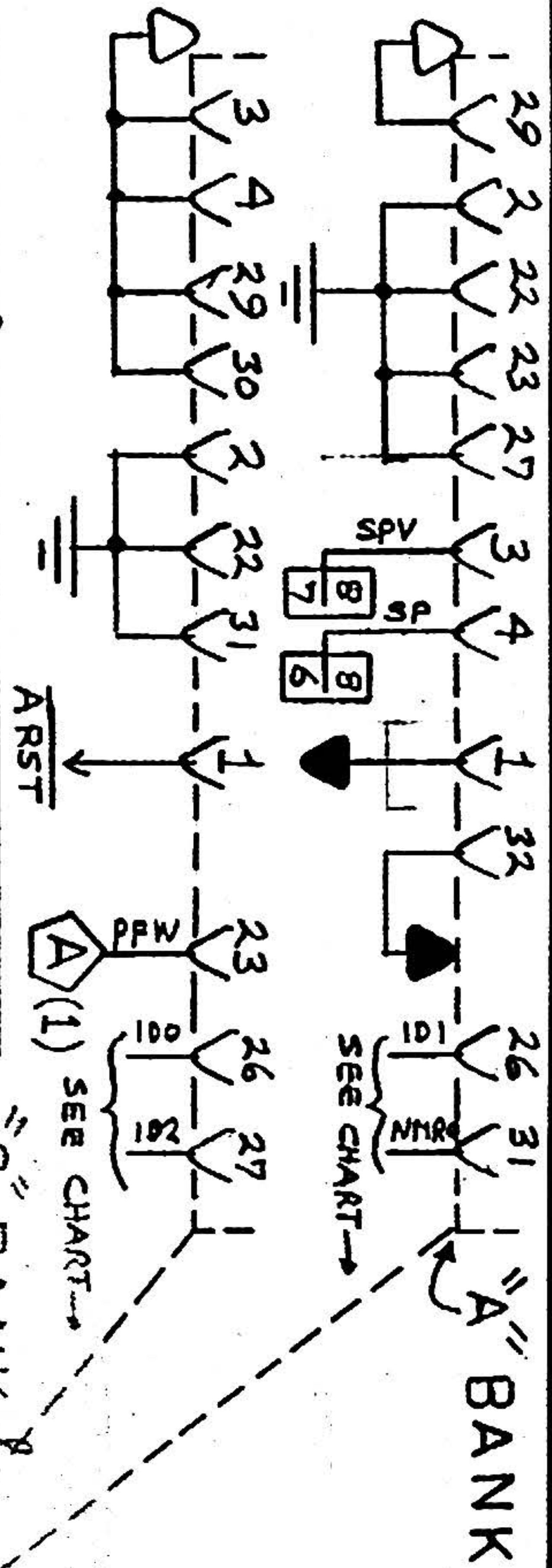
MEMORY LOADING:

MEG	BANK A	BANK B
1	4 256K	4 256K
2	4 256K	none
4	4 1M	4 256K
5	4 1M	4 1M
8	4 1M	4 1M

J10-J15
"B" BANK
all pins same for
all six sockets



VGCT219A6063



I.C. TYPE

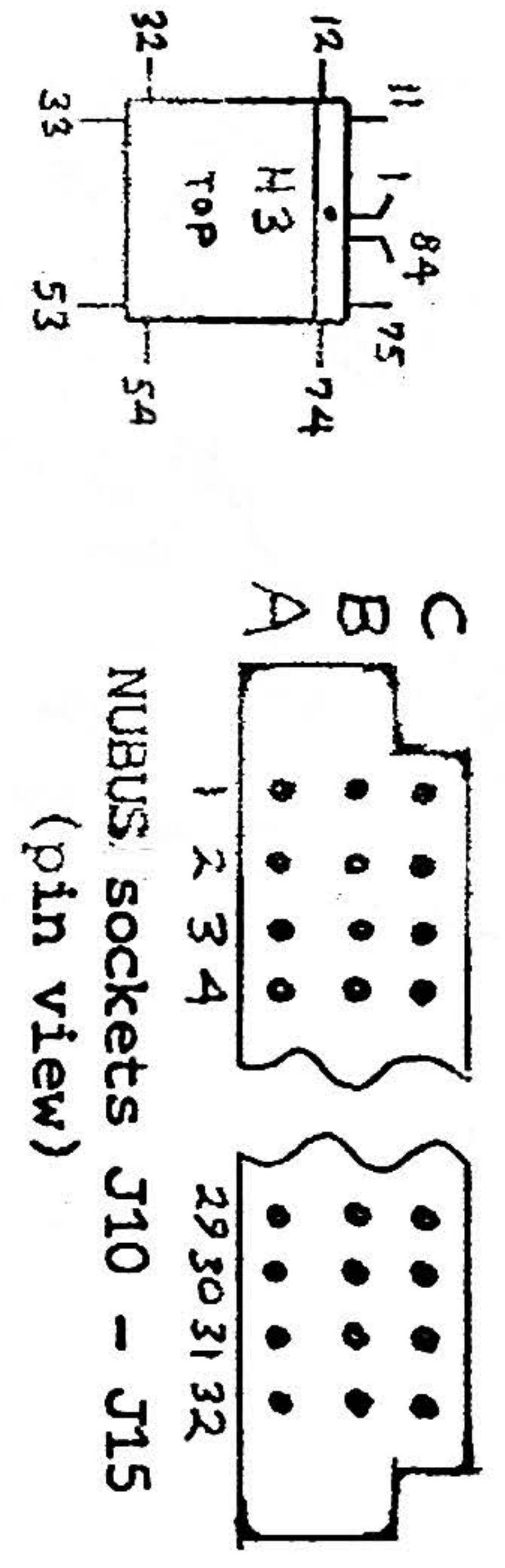
G1, H1	74 F	240	20	10
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RP# resistor arrays (sheet - 1)

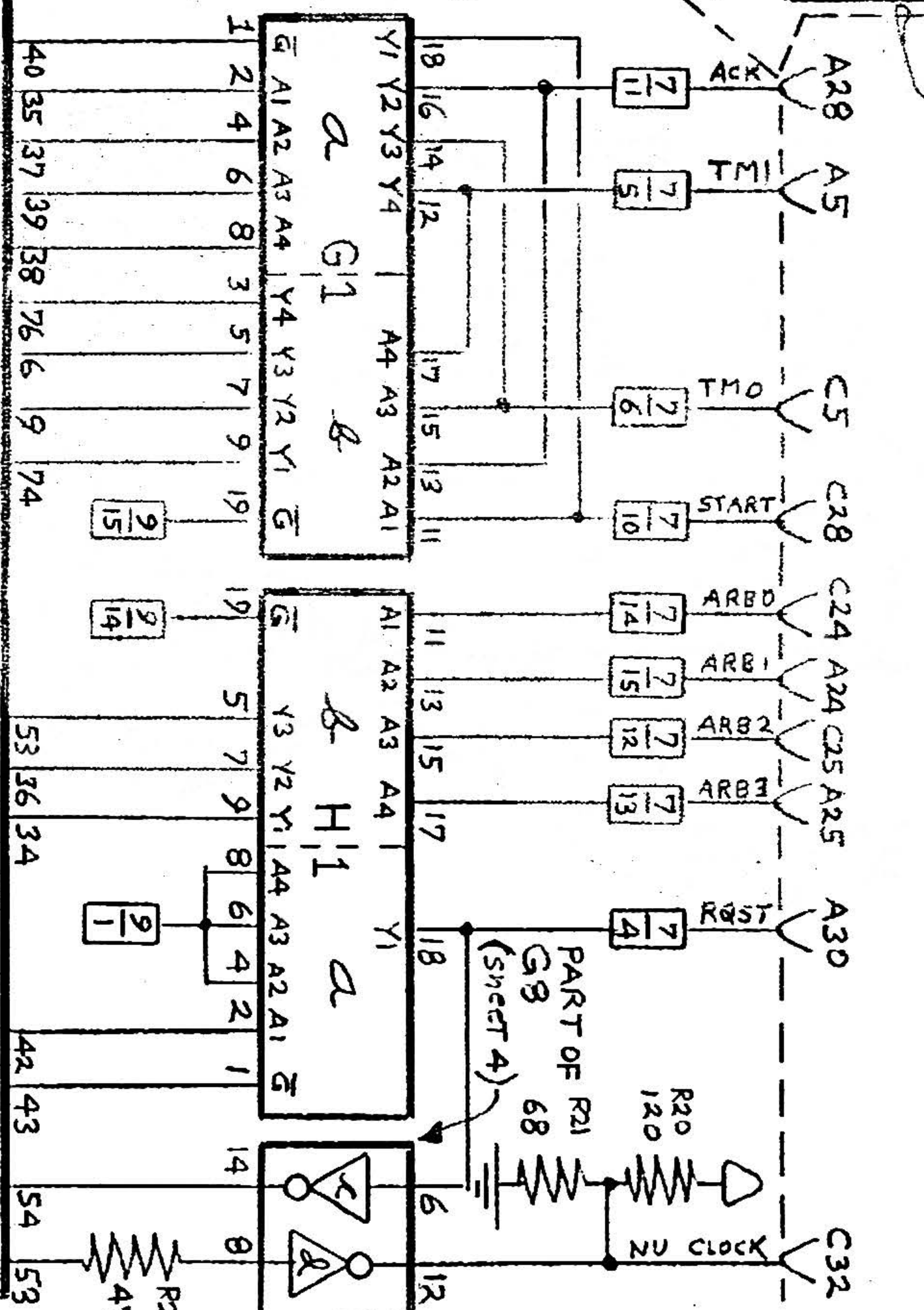
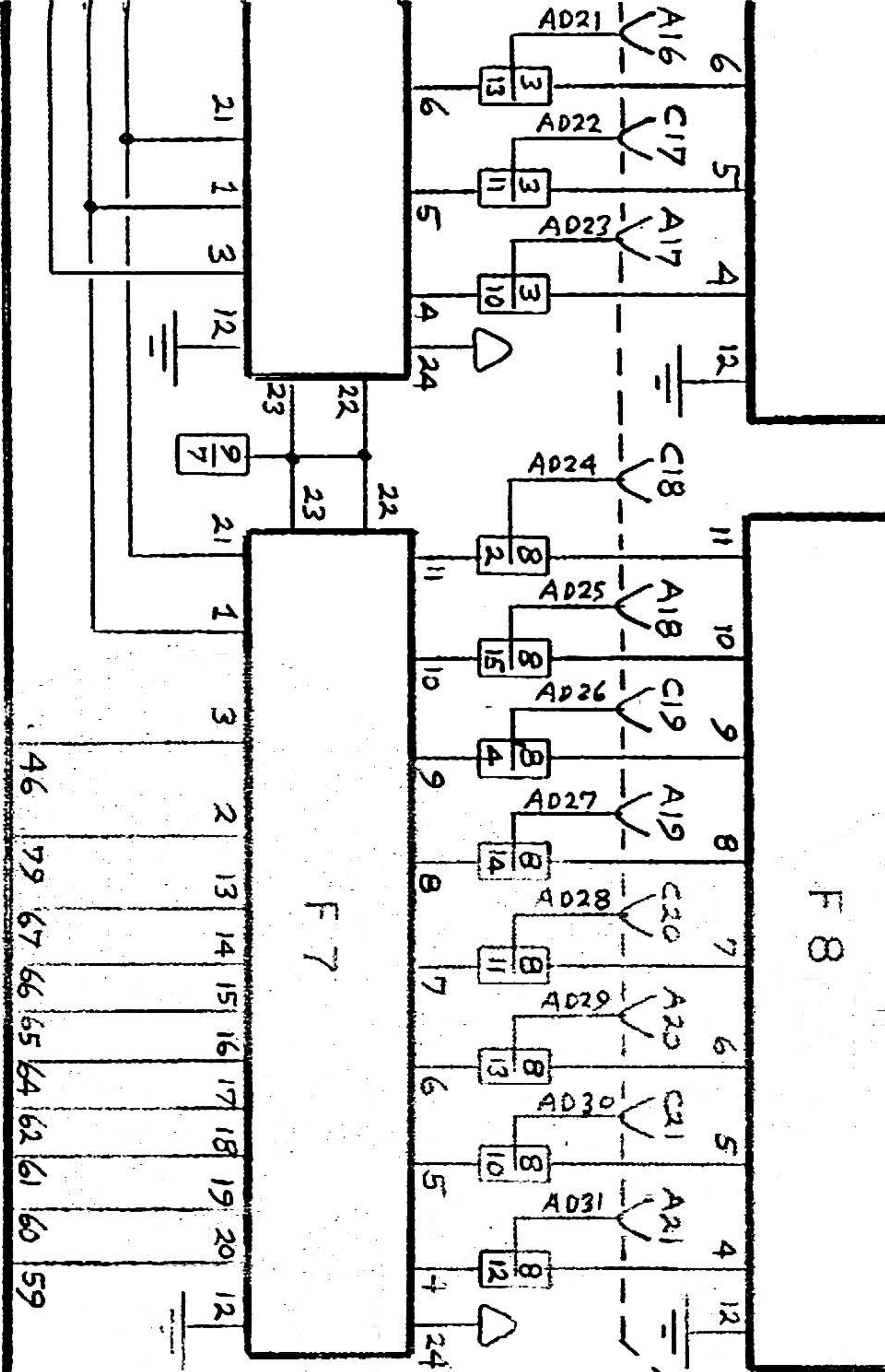
Pin#

SOCKET	A26	A31	C26	C27
J10 (slot 9)	0	2PA0	X	0
J11 (slot A)	X	2PA1	0	0
J12 (slot B)	X	2PA2	X	0
J13 (slot C)	0	2PA3	0	X
J14 (slot D)	0	2PA4	X	X
J15 (slot E)	X	2PA5	0	X

X = GROUNDED O = OPEN



DO-31 DATA
 D0 D1 D2 D3 D4 D5 D6 D7



(34450606-3)

NUCHIP

