Apple Net Names Reference Sheet

Circuit NetName	Description of Circuit
12C2_AP_BI_ALS_SDA_CONN	1 2C2 DATA COMMUNICATION BETWEEN THE MAIN CPU AND THE OPTICAL SENSOR
12CO_AP_SCL	12C INTERFACE CLOCK OF THE APPLIED PART
12S_AP_OWL_TO_CODEC_XSP_LRCLK	XSP CLOCK SIGNAL FROM THE OWL CIRCUIT OF THE MAIN CPU TO THE AUDIO
12S_AP_TO_BB_BCLK	CLOCK SIGNAL FROM THE MAIN CPU TO THE 12S INTERFACE OF THE BASEBAND CPU
12S_AP_TO_BB_DOUT	DATA OUTPUT FROM THE MAIN CPU TO THE 12S INTERFACE OF THE BASEBAND
12S_AP_TO_BT_DOUT	DATA OUTPUT FROM THE MAIN CPU TO THE 12S INTERFACE OF THE BLUETOOTH CHIP
12S_AP_TO_CODEC_ASP_BCLK	ASP CLOCK SIGNAL FROM THE MAIN CPU TO THE 12S INTERFACE OF THE AUDIO
12S_AP_TO_CODEC_MCLK_R	MAIN CLOCK SIGNAL FROM THE MAIN CPU TO THE 12S INTERFACE OF THE AUDIO
12S_AP_TO_CODEC_MSP_BC LK	CLOCK SIGNAL FROM THE MAIN CPU TO THE 12S VOICE COMMUNICATION OF THE
12S_AP_TO_CODEC_MSP_DOUT	DATA OUTPUT FROM THE MAIN CPU TO THE VOICE COMMUNICATION OF THE AUDIO
12S_AP_TO_CODEC_MSP_LRCLK	CLOCK SIGNAL FROM THE MAIN CPU TO THE VOICE COMMUNICATION OF THE AUDIO
12S_AP_TO_CODEC_XSP_DOUT	XSP DATA OUTPUT SIGNAL FROM THE MAIN CPU TO THE 12S INTERFACE OF THE AUDIO

Circuit NetName	Description of Circuit
12S_AP_TO_SPEAKERAMP_M CLK_R	CLOCK SIGNAL FROM THE MAIN CPU TO THE 12S INTERFACE OF THE AUDIO
12S_BB_TO_AP_DIN	DATA INPUT FROM THE BASEBAND TO THE 12S INTERFACE OF THE MAIN CPU
12S_CODEC_TO_AP_ASP_DIN	ASP DATA INPUT FROM THE AUDIO ENCODING AND DECODING TO THE 12S INTERFACE OF
12S_CODEC_TO_AP_MS P DIN	DATA INPUT FROM THE AUDIO ENCODING AND DECODING TO THE 12S VOICE
50_BBPMU_TO_STOCKHOLM_19P2M_CLK	19.2M CLOCK SIGNAL FROM THE BASEBAND POWER OUTPUT TO THE
50_MDM_19P2M_CLK	MODEM 19.2M CLOCK SIGNAL
50_MDM_19P2M_CLK_PMU	19.2M CLOCK SIGNAL FROM THE BASEBAND POWER OUTPUT TO THE BASEBAND CPU
50_SLEEP_CLK_32K sleep clock 32.768KHz	SIGNAL FROM THE BASEBAND POWER OUTPUT TO THE BASEBAND CPU
50_SLEEP_JLK_32K	SLEEP CLOCK 32.768KHZ
50_WTR_19P2M_CLK	19.2M CLOCK SIGNAL FROM THE BASEBAND POWER OUTPUT TO THE RF TRANSCEIVER
ACCEL	ACCELEROMETER
ACCEL GYRO	ACCELEROMETER GYROSCOPE
ACCELJNTL_L	ACCELERATOR INTERRUPT SIGNAL
ALS_INT_L	AMBIENT LIGHT INTERRUPT SIGNAL
ALS_TO_APINT_L	INTERRUPT LOW EFFECTIVE SIGNAL FROM THE LIGHT SENSOR TO THE MAIN CPU
ALSJNT_L	AMBIENT LIGHT DETECT ION INTERRUPT SIGNAL
AP_NTC	MAIN PROCESSOR TEMPERATURE DETECTION

Circuit NetName	Description of Circuit
AP_TO ARC_STAYIN_ALIVE	HOLD ACTIVATION SIGNAL FROM THE MAIN CPU TO THE AUDIO FREQUENCY AMPLIFIER
AP_TO_ARC_RESET_L	RESET SIGNAL FROM THE MAIN CPU TO THE AUDIO FREQUENCY AMPLIFIER
AP_TO_BB_MESA_ON_L	FINGERPRINT OPEN SIGNAL FROM THE MAIN CPU TO THE BASEBAND
AP_TO_BB_PCl E_DEV_WAKE	SERVICE WAKE-UP SIGNAL FROM THE MAIN CPU TO THE PCLE INTERFACE OF THE
AP_TO_BB_RADIO_ON_L	OPEN SIGNAL FROM THE MAIN CPU TO THE BASEBAND POWER SUPPLY OF THE BASEBAND
AP_TO_BB_RESET_L	RESET SIGNAL FROM THE MAIN CPU TO THE BASEBAND
AP_TO_BBPMU_RADIO_ON_L	BASEBAND SWITCHING SIGNAL FROM THE MAIN CPU TO THE BASEBAND POWER
AP_TO_BT_WAKE	WAKE-UP SIGNAL FROM THE MAIN CPU TO THE BLUETOOTH CHIP
AP_TO_FCAM_SHUTDOWN_L	CIOS ING SIGNAL FROM THE MAIN CPU TO THE FRONT CAMERA
AP_TO_FCAMJLK_R	CLOCK SIGNAL FROM THE MAIN CPU TO THE FRONT CAMERA
AP_TO_HP_HS3_CTRL	CONTROL SIGNAL FROM THE MAIN CPU TO THE EARPHONE MIC
AP_TO_LCM_RESET_L	RESET SIGNAL FROM THE MAIN CPU TO THE DISPLAY SCREEN
AP_TO_LED_DRIVER_E N	OPEN SIGNAL FROM THE MAIN CPU TO THE FLASH LIGHT DRIVE
AP_TO_LED_DRIVER_EN	OPEN SIGNAL FROM THE MAIN CPU TO THE FLASH CHIP
AP_TO_NAND_RESET_L	THE RESET SIGNAL FROM THE MAIN CPU TO THE HARD DISK

Circuit NetName	Description of Circuit
AP_TO_PMU_AMUX_OUT	ANALOG COMPOSITE SWITCH SIGNAL OUTPUT FROM MAIN CPU TO POWER CHIP
AP_TO_PMU_TEST_CLKOUT	THE TEST CLOCK SIGNAL OUTPUT FROM THE MAIN CPU TO THE MAIN POWER CHIP
AP_TO_PMU_WDOG_RESET	THE WATCHDOG RESET FROM THE MAIN CPU TO THE POWER CHIP
AP_TO_RCAM_SH UTDOWN	C1OSING SIGNAL FROM THE MAIN CPU TO THE REAR CAM ERA
AP_TO_RCAM_SHUTDOWN_L	CLOSING SIGNAL FROM THE MAIN CPU TO THE REAR CAMERA
AP_TO_SPEAKERAMP_RESET_L	RESET SIGNAL FROM THE MAIN CPU TO THE AUDIO FREQUENCY AMPLIFIER
AP_TO_STOCKHOLM_DEV_WAKE	SERVICE WAKE-UP SIGNAL FROM THE MAIN CPU TO THE NEAR FIELD
AP_TO_TOUCH_RESET_L	RESET SIGNAL FRO M THE MAIN CPU TO THE TOUCH
AP_TO_FCAM_CLK_CONN	FRONT CAMERA CLOCK SIGNAL SENT BY THE MAIN CPU
ARC_TO_APINT_L	INTERRUPT SIGNAL FROM THE AUDIO FREQUENCY AMPLIFIER TO THE MAIN CPU
AUDIO_ADIFFH_HP_AUD_DP	HEADPHONE OUTPUT
AUDIO CODEC	AUDIO ENCODING AND DECODING
BASEBAND BASEBAND	BASEBAND
BATIERY CONNECTOR	BATTERY PEDESTAL
BATIERY SWI	BATTERY SOFT INT ERRU PT INSTRUCTION
BATTERY_S NS	BATTERY VOLTAGE DETECTION SIGNAL
BB_EEPROM_12C_SDA	DATA SIGNAL OF 12C INTERFACE OF BASEBAND CHIP

Circuit NetName	Description of Circuit
BB_EEPROMJ2C_SCL	CLOCK SIGNAL OF 12C INTERFACE OF BASEBAND CHIP
BB_JTAG_SRST_L	BASEBAND JTAG RESET SIGNAL
BB_PMUJET_ON	BASEBAND POWER START SIGNAL
BB_RESET_DET_L	BASEBAND RESET DETECTION SIGNAL
BB_RESET_L	BASEBAND RESET SIGNAL
BB_TO_AP_GPS_TIME_MARK	GPS TIME STAMP SIGNAL FROM THE BASEBAND TO THE MAIN CPU
BB_TO_AP_RESET_DETECT_L	RESET DETECTION SIGNAL FROM THE BASEBAND TO THE MAIN CPU
BB_TO_PMU_PCIE_HOST_WAKE_L	THE HOST WAKE-UP LOW LEVEL EFFECTIVE SIGNAL FROM THE BASEBAND CPU TO
BB_WAKE_AP	BASEBAND WAKE
BOARd_iD2	MOTHERBOARD VERSION IDENTIFICATION
BOARD_ID4	MOTHERBOARD CONFIGURATION
BOOST_PROT	FEEDBACK
BOOST_SENSE_P	BRIGHTNESS ADJUSTMENT
BOOT_CO NFIG2	BOOT CONFIGURATION ITEM 2
BT_RESET_L	BLUETOOTH RESET SIGNAL
BT_TO_PMU_HOST_WAKE	THE HOST WAKE-UP SIGNAL FROM THE BLUETOOTH TO THE MAIN POWER
BT_WAKE	BLUETOOTH WAKE-UP SIGNAL
BT_WAKE_AP	BLUETOOTH WAKE-UP
BUTTON_RING ER_A	FAST SWITCHING SIGNAL FROM THE SILENCE TO VIBRATION
BUTTON_VOL_DOWN_L	VOLUME KEY “+”
CAM_CLK_SRC	CAMERA CLOCK SIGNAL
CAM_RESET_L	CAMERA RESET SIGNAL
CAM_SHUTDOWN	CAMERA SHUTDOWN SIGNAL

Circuit NetName	Description of Circuit
CAM_STROBE_EN	FLASH ENABLE SIGNAL
CAM_STROBE_EN	CAMERA SHUTDOWN ENABLE
CAM_VDDCORE_EN	CORE POWER SUPPLY ENABLE SIGNAL IN THE CAMERA
CLK32 K_GRAPE	MULTI TOUCH 32K CLOCK
CODEC_TO_AP_PMU_INT_L	INTERRUPT SIGNAL FROM THE AUDIO ENCODING AND DECODING TO THE APPLICATION
CODEC_TO_AP_PMU_INT_L	POWER INTERRUPT SIGNAL FROM THE AUDIO ENCODING AND DECODING TO THE MAIN CPU
CODEC_TO_HAC_P	DIFFERENTIAL TRANSMISSION N FROM THE AUDIO EN CODING AND DECODING TO THE HEARING AID
CODEC_TO_PMU_MIKEY_INT_L	DIGITAL RECORDING INTERRUPT LOW LEVEL EFFECTIVE SIGNAL FROM THE AUDIO
CODEC_TO_RCVR_N	DIFFERENTIAL TRANSMISSION SIGNAL N FROM THE AUDIO ENCODING AND DECODING TO THE
COMPASS	COMPASS
COMPASS_BRD_INT	COMPASS INTERRUPT SIGNAL
CYROJNT2	GYROSCOPE INTERRUPT SIGNAL
DIVERSITY ANTENNA SWITCH	ANTENNA SWITCH
EHCI_PORT_PWR	BUS FEEDBACK
FOCE2_VREF	CHIP SELECT REFERENCE VOLTAGE
FORCE DFU	FORCED DFU MODE
FOREHEAD_NTC	TEMPERATURE DETECTION ON THE TOP OF THE MOTHERBOARD
FRONTMIC3_TO_CODEC_AIN4_CONN_N	DIFFERENTIAL TRANSMISSION N FROM THE FRONT MIC3 OF THE
GRAPE_RESET_L	MULTI TOUCH RESET SIGNAL
GYRO GYROSCOPE	GYROSCOPE

Circuit NetName	Description of Circuit
GYRO_INT2	GYROSCOPE INTERRUPT SIGNAL
HOST_BB_HSIC_RDY	BASE BAND MAIN CONTROL SIGNAL
HPHONE_DET	EARPHONE DETECTION
HPHONE_OUT	EARPHONE SIGNAL OUTPUT
HPHONE_REF	EARPHONE REFERENCE VOLTAGE
HPHONE_REF_CTRL	EARPHONE REFERENCE VOLTAGE CONTROL
HPHONE_RET	EARPHONE SIGNAL RETURN DETECTION
I2S_AP_TO_BT_LRCLK	CLOCK SIGNAL FROM THE MAIN CPU TO THE 125 OF THE BLUETOOTH
I2S_BT_TO_AP_DIN	DATA INPUT SIGNAL FROM THE BLUETOOTH TO THE 125 OF THE MAIN CPU
L1NEOUTL_REF	EXTERNAL AUDIO REFERENCE VOLTAGE
LCD_BOOST_CTRL	BACKLIGHT ADJUSTMENT MANAGEMENT
LCD_BOOST_OUT	BACKLIGHT BOOST OUTPUT
LCD_BST_SW	BACKLIGHT TIME SWITCH
LCD_PWR_EN LCD	POWER ENABLE SIGNAL
LCD_RESET_L	LCD RESET SIGNAL
LCM_ISENSE	INDUCTION
LCM_TO_OWL_BSYNC	SYNCHRONIZATION SIGNAL FROM THE DISPLAY SCREEN TO THE MAIN PROCESSOR
LED_DRIVE_GSMB	LED DRIVING SIGNAL
LED_DRIVE_OUT	FLASH OUTPUT
LED_MODULE_NTC	FLASH ELEMENT TEMPERATURE DETECTION
LED_PWR_IN	BACKLIGHT VOLTAGE INPUT
LINE_OUT	EXTERNAL AUDIO OUTPUT

Circuit NetName	Description of Circuit
MAMBA_EXT_LDO_EN	FINGERPRINT SCANNING CIRCUIT EXTERNAL CONNECTION LDO CHIP OPEN SIGNAL
MENU_KEY_L	RETURN KEY(HOME)
MESA_TO_AP_INT	INTERRUPT SIGNAL FROM THE FINGERPRINT TO THE MAIN CPU
MIPI_AP_TO_LCM_DATAO_N	TRANSMIT DATA FROM THE MA IN CPU TO THE MIPI INTERFACE OF THE DISPLAY SCREEN
MIPI_FCAM_TO_AP_CLK_CONN_N	TRANSMISSION CLOCK DIFFERENTIAL SIGNAL N FROM THE FRONT CAMERA TO THE
MIPI_FCAM_TO_AP_DATA1_CONN_P	DATAL GROUP OF TRANSMISSION DIFFERENTIAL SIGNAL P FROM THE FRONT
MIPI_FCAM_TO_AP_DATAO_CONN_N	DATA 0 GROUP OF TRANSMISSION DIFFERENTIAL SIGNAL FROM THE FRONT
MIPI_FCAM_TO_AP_DATAO_P	TRANSMIT DATA FROM THE FRONT CAMERA TO THE MIPI INTERFACE OF THE MA IN CPU
MIPI_RCAM_TO_AP_DATAO_CONN_P	TRANSMIT DATA FROM THE REAR CAMERA TO THE MIPI INTERFACE OF THE
MIPIJCAM_TO_AP_C LK_CONN_P	TRANSMISSION CLOCK DIFFERENTIAL SIGNAL P FROM THE FRONT CAMERA TO THE
MMPA_2G3G_MODE	POWER AMPLIFIER MODE CONTROL SIGNAL
NIMBUS_VDDH_TEST	MULTI TOUCH TEST SIGNAL
NTC_CAM _P	CAMERA NTC DETECTION SIGNAL
NTC_H4P_P NTC	DETECT ION SIGNAL IN THE AT PART
NTC_RFPA_P	RADIO FREQUENCY NTC DETECTION SIGNAL

Circuit NetName	Description of Circuit
OSCAR_BI_AP_TIME_SYNC_HOSTJNT	TIME SYNCHRONIZATION HOST INTERRUPT SIGNAL BETWEEN CO - PROCESSOR
OSCAR_TO_PHOSPHORUS_SPI_CS_L	CHIP SELECT LOW LEVEL EFFECTIVE SIGNAL FROM THE CO-PROCESSOR TO THE AIR
OSCAR_TO_COMPASS_SPI_CS_L	CHIP SELECT LOW LEVEL EFFECTIVE SIGNAL FROM THE CO-PROCESSOR TO THE SPI
PCIE_AP_TO_NAND_REFCLK_P	REFERENCE CLOCK SIGNAL FROM THE MAIN CPU TO THE PCIE INTERFACE OF THE HARD
PCIE_AP_TO_NAND_RESET_L	RESET SIGNAL FROM THE MAIN CPU TO THE PCIE INTERFACE OF THE HARD DISK
PCIE_AP_TO_NAND_TXDO_P	THE EMIT DATA FROM THE MAIN CPU TO THE PCIE INTERFACE OF THE HARD DISK
PCIE_AP_TO_WLAN_DEV_WAKE	SERVICE WAKE-U P SIGNAL FROM THE MAIN CPU TO THE PCIE INTERFACE OF THE
PCIE_BB_BI_AP_CLKREQ_L	CLOCK REQUEST LOW VALID SIGNAL FROM THE BASEBAND TO THE PCIE INTERFACE OF THE
PCIE_NAND_TO_AP_CLKREQ	CLOCK REQUEST SIGNAL FROM THE HARD DISK TO THE PCLE INTERFACE OF THE MAIN CPU
PCIE_NAND_TO_AP_RXDO_P	THE RECEIVE DATA FROM THE HARD DISK TO THE PCLE INTERFACE OF THE MAIN CPU
PCIE_AP_TO_WLAN_RESET_L	RESET SIGNAL FROM THE MAIN CPU TO THE PCLE INTERFACE OFTHE WIFI CHIP
PGND_IRLED_K	INFRARED LIGHT EMITTING DIODE CATHODE GROUNDING

Circuit NetName	Description of Circuit
PLL AVDD	PHASE-LOCKED LOOP FEEDBACK CONTROL CIRCUIT
PMIC_RESOUT_L	RESET LOW LEVEL ACTIVE SIGNAL OUTPUT BY BASEBAND POWER
PMU_RESET_IN	POWER MANAGEMENT RESET INPUT
PMU_TCAL	TEMPERATURE CORRECTION OF THE POWER CHIP CIRCUIT
PMU_TO_APIRQ.L	INTERRUPT REQUEST SIGNAL FROM THE MAIN POWER CHIP TO THE MAIN CPU
PMU_TO_BB_PMIC_RESET_R_L	RESET SIGNAL FROM THE MAIN POWER CHIP TO THE BASEBAND POWER
PMU_TO_BB_USB_VBUS_DETECT	DETECTION SIGNAL FROM THE MAIN POWER CHIP TO THE USB 5V OF THE
PMU_TO_BBPMU_RESET_L	RESET SIGNAL FROM THE MAIN POWER CHIP TO THE BASEBAND POWER
PMU_TO_CODEC_DIGLDO_PULLDN	DIGITAL LDO PULL DOWN FROM THE MAIN POWER CHIP TO THE AUDIO ENCODING
PMU_TO_NAN D_LOW_BATI_BOOT_L	BATTERY LOW VOLT AGE START LOW LEVEL EFFECTIVE SIGNAL FROM THE MA IN
PMU_TO_STOCK HOLM_EN	OPEN SIGNAL FROM THE POWER CHIP TO THE NEAR FIE LD COMMUNICATION CHIP
PMU_TO_WLAN_REG_ON	OPEN SIGNAL FROM THE POWER CHIP TO THE POWER SUPPLY OF THE WIFI
PMUJRQ_L	POWER INTERRUPT REQUEST SIGNAL
PP_BATT_VCC	BATTERY POWER SUPPLY VOLTAGE
PP_CODEC_TOFRONTMIC3_BIAS_CONN	BIAS POWER SUPPLY FROM THE AUDIO ENCODING AND DECODING TO THE
PP_LED_DRIVER_COOL_LED	FLASH LIGHT COLD LIGHT DRIVING POWER SUPPLY

Circuit NetName	Description of Circuit
PP_LED_DRIVER_WARM_LED	FLASH LIGHT WARM LIGHT DRIVING POWER SUPPLY
PP_QPOET_VCC_PA	POWER AMPLIFIER POWER SUPPLY CHIP OUTPUT THE POWER SUPPLY TO THE POWER AMPLIFIER
PP_QPOET_VDD_BOOST_OUT	BOOST OUTPUT OF POWER AMPLIFIER POWER SUPPLY CHIP
PP_SPHERE	FOCUSING DRIVE POWER SUPPLY
PP_UIM1_LDOll	SIM CARD POWER SUPPLY 1.8V
PP_VCC_MAIN	MAIN POWER SUPPLY VOLTAGE
PP3VO_PROX_CONN	3V POWER SUPPLY OF THE PROXIMITY SENSOR ON THE PEDESTAL
PP3VO_PROXJRLED	3V POWER SUPPLY OF THE INFRARED PROXIMITY SENSOR
PP5VO_USB_PROTECT	USB CHARGING VOLTAGE
PPlV2_FCAM_VCORE_CONN	CORE POWER SUPPLY 1.2V OF THE FRONT CAMERA
PPlV8_FCAM_CONN	1.8V POWER SUPPLY OF THE FRONT CAMERA
PROX & ALS INTE RFACE	PROXIMITY SENSOR
PROX_RX	PROXIMITY SENSOR RX SIGNAL
PROX_RX_EN	PROXIMITY SENSOR RX ENABLE
PROX_TX_EN	PROXIMITY SENSOR TX ENABLE
PS_HOLD	MAINTAIN SIGNAL
PS_HOLD_PMIC	MAINTAIN SIGNAL SENT BY BASEBAND CPU TO THE BASEBAND POWER
RADIO_ON_L	POWER START SIGNAL
RADIO_PA_NTC	BASEBAND POWER AMPLIFIER TEMPERATURE DETECTION
RCAM_TO_AP_MIPI_DATA3_P	TRANSMISSION DATA FROM THE REAR CAMERA TO THE MIPI INTERFACE OF THE MAIN

Circuit NetName	Description of Circuit
RCAM_TO_LEDDRV_STROBE_EN	FLASH OPEN SIGNAL FROM THE REAR CAMERA TO THE FLASH CHIP
RCVR_TEST	RECEIVER TEST
REAR_CAMER_NTC	REAR CA MERA CIRCUIT TEMPERA TU RE DETECTION
RESISTOR FOR TEMP CALIBRATION	THERMISTOR SENSOR
RF	ANYTHING THAT HAS RF HAS TO DO WITH RADIO FREQUENCY
RFFE_CLOCK_FILTERS	CLOCK SIGNAL FILTERING PART OF THE FRONT END OF THE RF
RINGER_A	MUTE SWITCH BUTTON
RVCR_CONN_N	RECEIVER
SIM_CLK	SIM CARD CLOCK SIGNAL
SIM_DETECT	SIM CARD DETECTION SIGNAL
SIM_RST	SIM CARD RESET SIGNAL
SIM_TRAY_DET	SIM CARD INSERTION TEST SIGNAL
SIMCRD_RST SIM CARD RESET SIGNAL	SIM CARD RESET SIGNAL
SLEEP_CLK MAIN TALK SIGNAL	MAIN TALK SIGNAL
SLEEP_CLK SLEEP CLOCK	SLEEP CLOCK
SO_LB_ASM_ANTl_LAT	SIGNAL FROM THE LOW FREQUENCY BAND ANTENNA TO THE ANTENNA
SO_MB-HB_ASM_ANTl_LAT	SIGNAL FROM MIDDLE AND HIGH FREQUENCY BAND ANTENNA SWITCH TO THE ANTENNA
SPEAKERAMP_TO_AP_INT_L	INTERRUPT SIGNAL FROM THE AUDIO FREQUENCY AMPLIFIER TO THE MAIN CPU
SPI_AP_TO_CODEC_CS_L	CHIP SELECT FROM THE MAIN CPU TO THE SPI OF AUDIO ENCODING AND DECODING CHIP
SPI_AP_TO_CODEC_MOSI	THE MAIN OUTPUT FROM INPUT FROM THE MAIN CPU TO THE SPI OF THE AUDIO ENCODING

Circuit NetName	Description of Circuit
SPI_AP_TO_CODEC_SCLK	CLOCK SIGNAL FROM THE MAIN CPU TO THE SPI OF AUDIO ENCODING AND DECODING CHIP
SPI_AP_TO_MESA_MOSI	THE MAIN OUTPUT FROM THE INPUT SIGNAL FROM THE MAIN CPU TO THE SPI INTERFACE OF
SPI_AP_TO_MESA_SCLK_R	CLOCK SIGNAL FROM THE MAIN CPU TO THE SPI INTERFACE OF THE FINGERPRINT CHIP
SPI_AP_TO_TOUCH_CS_L	CHIP SELECT LOW LEVEL EFFECTIVE SIGNAL FROM THE MAIN CPU TO THE 5P I INTERFACE OF
SPI_AP_TO_TOUCH_MOSI	THE MAIN OUTPUT FROM THE INPUT SIGNAL FROM THE MAIN CPU CHIP TO THE SPI
SPI_AP_TO_TOUCH_SCLK_R	CLOCK SIGNAL FROM THE MAIN CPU TO THE SPI INTERFACE OF THE TOUCH
SPI_CODEC_TO_AP_miSO	THE MAIN OUTPUT FROM INPUT FROM THE AUDIO ENCODING AND DECODING TO THE SPI
SPI_MESA_TO_AP_miSO	THE MAIN INPUT FROM THE OUTPUT SIGNAL FROM THE FINGERPRINT TO THE SPI INTERFACE OF
SPI_TOUCH_TO_AP_MISO	THE MAIN INPUT FROM OUTPUT SIGNAL FROM THE TOUCH TO THE SPI INTERFACE OF THE
SPK AMP	LOUDSPEAKER AMPLIFIER
SPKAMP_EN	SPEAKER PER-AMPLIFIER ENABLE SIGNAL
SPKR_CONN_PREAMP _P	PER-AMPLIFIER OUTPUT TO RING AMPLIFIER
STOCKHOLM_TO_PMU_HOST_WAKE	THE HOST WAKE-UP SIGNAL FROM THE NEAR FIELD COMMUNICATION CHIP TO
SW_BOOST	BACKLIGHT SWITCH

Circuit NetName	Description of Circuit
SWI_AP_BI_TIGRIS	ELECTRIC QUANTITY DETECTING SIGNAL FROM THE MAIN CPU TO THE CHARGING CHIP
SWI_AP_BI_TIGRIS_FET	ELECTRON IC QUANTITIES DETECTION SIGNAL BETWEEN THE MAIN CPU AND THE CHARGING CHIP
TIGRIS CHARGER	CHARGE MANAGEMENT CHIP
TIGRIS_ACTIVE_DIODE	CHARGE TUBE ACTIVATION SIGNAL
TIGRIS_TO_BATIERY_SWI	ELECTRONIC QUANTITIES DETECTION SIGNAL FROM THE CHARGING CHIP TO THE BATTERY
TIGRIS_TO_PMUINT_R_L	INTERRUPT SIGNAL FROM THE CHARGING CHIP TO THE MAIN POWER CHIP
TIGRIS_VBUS_DETECT	CHARGING SV DETECTION
TOUCH_TO_APINT_L	INTERRUPT LOW EFFECTIVE SIGNAL FROM THE TOUCH TO THE MAIN CPU CHIP
TOUCH_TO_PROX_RX_EN_FCAM_CONN	RECEIVE OPEN SIGNAL FROM THE TOUCH CHIP TO THE PROXIMITY SENSOR
TOUCH_TO_PROX_TX_EN_BUFF	SEND AND OPEN BUFFER SIGNAL FROM THE TOUCH TO THE PROXIMITY SENSING
TRISTAR_TO_APINT	INTERRUPT SIGNAL FROM THE USB MANAGER TO THE MAIN CPU
TRISTAR_TO_TIGRIS_VBUS_OFF	CLOSING SIGNAL OF THE CHARGING CHIP SV OVER-VOLTAGE PROTECTION
UART_ACCESSORY_TO_AP_RXD	RECEIVING DATA FROM THE ATTACHMENT TO THE UART SERIAL OF THE MAIN CPU
UART_AP_DEBUG_RXD	THE MAIN PROCESSOR SECTION DEBUG INTERFACE OF THE UART SERIAL RECEIVES DATA

Circuit NetName	Description of Circuit
UART_AP_TO_ACCESSORY_TXD	TRANSMISSION DATA FROM THE MAIN CPU TO THE UART SERIAL OF THE USB
UART_AP_TO_BT_RTS_L	UART SERIAL FROM THE MAIN CPU TO THE BLUETOOTH CHIP SEND S REQUEST SIGNAL
UART_AP_TO_BT_TXD	SENDING DATA FROM THE MAIN CPU TO THE UART INTERFACE OF THE BLUETOOTH CHIP
UART_AP_TO_STOCKHO_LM_TXD	TRANSMISSION DATA FROM THE MAIN CPU TO THE UART SERIAL OF THE NEAR FIELDN COMMUNICATION
UART_AP_TO_STOCKHOLM_RTS_L	UART SERIAL FROM THE MAIN CPU TO THE NEAR FIELD COMMUNICATION CHIP
UART_AP_TO_WLAN_RTS_L	UART SERIAL FROM THE MAIN CPU TO THE WIFI CHIP SENDS REQUEST SIGNAL
UART_AP_TO_WLAN_TXD	TRANSMISSION DATA FROM THE MAIN CPU TO THE UART SERIAL OF THE WIFI CHIP
UART_BT_TO_AP_CTS_L	UART SERIAL FROM THE BLUETOOTH TO THE MAIN CPU CLEARS SENDING SIGNAL
UART_STOCKHO LM_TO_AP_RXD	RECEIVING DATA FROM THE NEAR FIELD COMMUNICATION CHIP TO THE UART
UART_STOCKHOLM_TO_AP_CTS_L	UART SERIAL FROM THE MAIN CPU TO THE NEAR FIELD COMMUNICATION CHIP
UART_WLAN_TO_AP_RXD	RECEIVING DATA FROM THE WI FI CHIP TO THE UART SERIAL OF THE MAIN CPU
UART_WLAN_TO_APITS_L	UART SERIAL FROM THE MAIN CPU TO THE WI FI CHIP CLEARS SENDING SIGNAL

Circuit NetName	Description of Circuit
USB_AP_DATA_N	THE DATA FROM THE USB TO THE MAIN CPU
USB_VBUS	USB REFERENCE VOLTAGE INPUT
USB_VBUS_PROT _SNS	USB CHARGING DETECTION
USBHS_P\USB FS_P	USB COMMUNICATION INTERFACE
VBATI_SENSE	BATTERY QUANTITIES DETECTION AND TRANSMISSION
VBOOST_LCM	LCD DISPLAY MAIN POWER
VIB_LDO_EN	VIBRATOR POWER SUPPLY ENABLE
VIB_PWM	VIBRATOR DRIVE SIGNAL
VIDEO_AMP_EN	EARPHONE REFERENCE VOLTAGE CONTROL
VOL_DWN_L	VOLUME DOWN
VOL_UP_L	VOLUME UP
W LAN_WAKE_AP	WLAN WAKEUP
WLAN LAT 2.4GHZ BAW BPF	WIFI 2.4GHZ BAND PASS FILTERING CIRCUIT
WLAN_HSIC_RDY	WIRELESS CONTROL SIGNAL
WLAN_TO_PMU_HOST_WAKE	THE HOST WAKE-UP SIGNAL FROM THE WIFI CHIP TO THE MAIN POWER
XO_OUT_D1_EN	CLOCK OUTPUT ENABLE SIGNAL
XO_OUT_DO_EN	BASEBAND CLOCK OUTPUT SIGNAL
XTAL_19P2M_OUT	19.2MHZ CLOCK SIGNAL OUTPUT
XTAL_19P2MJN	19.2MHZ CLOCK SIGNAL INPUT
XTAL_API4M_OUT	THE 24M CLOCK SIGNAL OUTPUT OF THE MAIN CPU